NOV 1 3 2006



Attorney's Doc. No. 9898-317 Client Ref. No. SS-18715-US

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent No.:

US 6,972,597 B2

Inventor:

Kim

Issued:

December 6, 2005

For:

SIMULTANEOUS BIDIRECTIONAL INPUT/OUTPUT CIRCUIT AND

**METHOD** 

Ser. No.:

10/748,748

Filed:

December 29, 2003

Group Art Unit:

2819

Examiner:

Vibol Tan

#### TRANSMITTAL LETTER

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 Certificate
NOV 1 5 2006

Enclosed for filing are the following:

Request for Certificate of Correction

Certificate of Correction Form PTO 1050 (in duplicate)

Return postcard

Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.

Hosoon Lee Reg. No. 56,737

MARGER JOHNSON & McCOLLOM, P.C. 210 SW Morrison Street, Suite 400 Portland, OR 97204 503-222-3613

Customer No. 20575

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

22313-1430

Date: November  $\mathcal{L}$ , 200

Kelly Andrews

**PATENT** Attorney's Doc. No. 9898-317 Client Ref. No. SS-18715-US

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Vibol Tan

### REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Upon comparison of the above-identified original Letters Patent with applicant's copy of the application therefore, the following error in the original Letters Patent has been noted:

At column 1, line 61 please replace " $V_1$ " with -- $V_{\ell}$ -- At column 1, line 63 please replace " $V_1$ " with -- $V_{\ell}$ -- At column 1, line 65 please replace " $V_1$ " with -- $V_{\ell}$ --

At column 8, line 3 please replace "12" with --I2--

At column 8, line 16 please replace "N1." with --N10.--

At column 8, line 55 please replace "13" with -- I3--

At column 9, line 66 please replace "field-effect of transistors" with --field-effect transistors of--

At column 11, line 34 please replace "receive the" with --receives the--

At column 12, line 16 please replace "current supplying" with --current; supplying--

Applicant respectfully requests that a Certificate of Correction calling attention to the above error be issued pursuant to Rule 322 of the Patent Office and Title 35, United States Code, Section 254. Two copies of a Certificate of Correction form are enclosed.

Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.

Hosoon Lee

Reg. No. 56,737

MARGER JOHNSON & McCOLLOM, P.C. 210 SW Morrison Street, Suite 400 Portland, OR 97204 503-222-3613

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Date: November \_\_\_\_\_\_\_\_, 2

\_, 2006

Kelly Andrews

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(Also Form PTO-1050)

## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO

US 6,972,597 B2

DATED

December 6, 2005

INVENTOR(S):

Kim

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

At column 1, line 61 please replace "V1" with --Vt--

At column 1, line 63 please replace "V1" with --Vt--

At column 1, line 65 please replace " $V_1$ " with -- $V_{t}$ --

At column 8, line 3 please replace "12" with --I2--

At column 8, line 16 please replace "N1." with --N10.--

At column 8, line 55 please replace "13" with -- I3--

At column 9, line 66 please replace "field-effect of transistors" with --field-effect transistors of--

At column 11, line 34 please replace "receive the" with --receives the--

At column 12, line 16 please replace "current supplying" with --current; supplying--

MAILING ADDRESS OF SENDER: Marger Johnson & McCollom 210 S.W. Morrison Street, Suite 400 Portland, Oregon 97204 PATENT NO. <u>US 6,972,597 B2</u>

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# UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO

US 6,972,597 B2

DATED

December 6, 2005

INVENTOR(S) :

Kim

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

At column 1, line 61 please replace "V1" with --V1-

At column 1, line 63 please replace "V<sub>1</sub>" with --V<sub>ℓ</sub>--

At column 1, line 65 please replace "V1" with --V2--

At column 8, line 3 please replace "12" with --I2--

At column 8, line 16 please replace "N1." with --N10.--

At column 8, line 55 please replace "13" with -- I3--

At column 9, line 66 please replace "field-effect of transistors" with --field-effect transistors of-

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